

Functional selection guide

LVT16 family

MULTI-BIT FLIP FLOPS

FUNCTION	DEVICE NUMBER	CLOCK EDGE	SET	RESET	OUTPUT
16-bit D-type, 3-state	74LVT16374A	positive	–	–	NINV
16 bit D-type, 30 Ω series terminated outputs, 3-state	74LVT162374	positive	–	–	NINV

MULTI-BIT LATCHES

FUNCTION	DEVICE NUMBER	RESET LEVEL	ENABLE LEVEL	OUTPUT
16 bit transparent latch, 3-state	74LVT16373A	LOW	HIGH	NINV
16 bit transparent latch with 30 Ω series terminated outputs, 3-state	74LVT162373	LOW	HIGH	NINV

MULTI-BIT REGISTERS

FUNCTION	DEVICE NUMBER	LATCH ENABLE	OUTPUT
16 bit registered transceiver with dual enable, 3-state	74LVT16543A	LOW	NINV
16 bit transceiver/register, 3-state	74LVT16646A	–	NINV
16 bit transceiver/register, 3-state	74LVT16652A	–	NINV

MULTI-BIT BUFFERS, DRIVERS & TRANSCEIVERS

FUNCTION	DEVICE NUMBER	ENABLE LEVEL	OUTPUT
16 bit buffer/driver, inverting, 3-state	74LVT16240A	4 LOW	INV
16 bit buffer/driver with 30 Ω series terminated outputs, inverting, 3-state	74LVT162240A	4 LOW	INV
16 bit buffer/driver, 3-state	74LVT16244B	4 LOW	NINV
16 bit buffer/driver with 30 Ω series terminated outputs, 3-state	74LVT162244B	4 LOW	NINV
16 bit transceiver, 3-state	74LVT16245B	2 LOW	NINV
16 bit transceiver with 30 Ω series terminated outputs, 3-state	74LVT162245B	2 LOW	NINV
18 bit universal bus transceiver, 3-state	74LVT16500A	1 LOW + 1 HIGH	NINV
18 bit universal bus transceiver, 3-state	74LVT16501A	1 LOW + 1 HIGH	NINV