

## Functional selection guide

## LVT family

## GATES

FUNCTION		DEVICE NUMBER
INVERTERS	Hex inverter	74LVT04
	Hex inverter Schmitt trigger	74LVT14
NAND	Quad 2 input	74LVT00
	Triple 3 input	74LVT10
	Dual 4 input	74LVT20
AND	Quad 2 input	74LVT08
NOR	Quad 2 input	74LVT02
OR	Quad 2 input	74LVT32
Exclusive OR	Quad 2 input	74LVT86

## FLIP FLOPS

FUNCTION	DEVICE NUMBER	CLOCK EDGE	SET	RESET	OUTPUT
Dual D-type	74LVT74	positive	LOW	LOW	NINV, INV
Octal D-type	74LVT273	positive	–	LOW	NINV
Octal D-type, 3-state	74LVT374	positive	–	–	NINV
Octal D-type, 3-state	74LVT534	positive	–	–	INV
Octal D-type, 3-state	74LVT574	positive	–	–	NINV

## LATCHES

FUNCTION	DEVICE NUMBER	RESET LEVEL	ENABLE LEVEL	OUTPUT
Octal D-type transparent latch, 3-state	74LVT573	LOW	HIGH	NINV

## REGISTERS

FUNCTION	DEVICE NUMBER	LATCH ENABLE	OUTPUT
Octal register transceiver with dual enable, 3-state	74LVT543	LOW	NINV
Octal bus transceiver/register, 3-state	74LVT646	–	NINV
Octal transceiver/register, 3-state	74LVT652	–	NINV
Octal registered transceiver, 3-state	74LVT2952	–	NINV

## BUFFERS, DRIVERS &amp; TRANSCEIVERS

FUNCTION	DEVICE NUMBER	ENABLE LEVEL	OUTPUT
Quad buffer/line driver, 3-state	74LVT125	LOW	NINV
Quad buffer/line driver, 3-state	74LVT126	HIGH	NINV
Octal inverting buffer, 3-state	74LVT240	2 LOW	INV
Octal buffer/line driver, 3-state	74LVT241	1 LOW + 1 HIGH	NINV
Octal buffer/line driver with 30 $\Omega$ series terminated outputs, 3-state	74LVT2241	1 LOW + 1 HIGH	NINV
Octal buffer/line driver, 3-state	74LVT244A	2 LOW	NINV
Octal buffer/line driver, 3-state	74LVT244B	2 LOW	NINV
Octal buffer/line driver with 30 $\Omega$ series terminated outputs, 3-state	74LVT2244	2 LOW	NINV
Octal transceiver with direction pin, 3-state	74LVT245	1 LOW	NINV
Octal transceiver with direction pin, 3-state	74LVT245B	1 LOW	NINV
Octal transceiver with direction pin, 30 $\Omega$ series terminated outputs, 3-state	74LVT2245	1 LOW	NINV
Octal Transceiver with dual enable, non-inverting, 3-state	74LVT623	1 LOW + 1 HIGH	NINV
Octal transceiver with direction pin, inverting, 3-state	74LVT640	1 LOW	INV
Combined 8-bit receiver and 4-bit bus driver	74LVT1403	1 LOW, 2 HIGH or LOW	NINV, INV