

Functional selection guide

ALVT family

MULTI-BIT FLIP FLOPS

FUNCTION	DEVICE NUMBER	CLOCK EDGE	SET	RESET	OUTPUT
16 bit D-type, 3-state	74ALVT16374	positive	–	–	NINV
20 bit D-type, 3-state	74ALVT16821	positive	–	–	NINV
20 bit D-type with 30 Ω series terminated outputs, 3-state	74ALVT162821	positive	–	–	NINV
18 bit D-type, 3-state	74ALVT16823	positive	–	LOW	NINV
18 bit D-type with 30 Ω series terminated outputs, 3-state	74ALVT162823	positive	–	LOW	NINV

MULTI-BIT LATCHES

FUNCTION	DEVICE NUMBER	RESET LEVEL	ENABLE LEVEL	OUTPUT
16 bit transparent latch, 3-state	74ALVT16373	LOW	HIGH	NINV
20 bit bus interface latch, 3 state	74ALVT16841	LOW	HIGH	NINV

MULTIPLEXERS

FUNCTION	DEVICE NUMBER	SELECT INPUTS	ENABLE LEVEL	OUTPUT
12 bit to 24 bit multiplexed D-type latches, 3-state	74ALVT16260	HIGH	3 LOW	NINV

MULTI-BIT REGISTERS

FUNCTION	DEVICE NUMBER	LATCH ENABLE	OUTPUT
16 bit registered transceiver with dual enable, 3-state	74ALVT16543	LOW	NINV
16 bit transceiver/register, 3-state	74ALVT16646	–	NINV
16 bit transceiver/register, 3-state	74ALVT16652	–	NINV
16 bit transceiver/register, 3-state	74ALVT16952	–	NINV
16 bit transceiver/register, 3-state	74ALVT16953	–	NINV

MULTI-BIT BUFFERS, DRIVERS & TRANSCEIVERS

FUNCTION	DEVICE NUMBER	ENABLE LEVEL	OUTPUT
16 bit buffer/driver, inverting, 3-state	74ALVT16240	4 LOW	INV
16 bit buffer/driver with 30 Ω series terminated outputs, inverting, 3-state	74ALVT162240	4 LOW	INV
16 bit buffer/driver, 3-state	74ALVT16241	2 LOW + 2 HIGH	NINV
16 bit buffer/driver with 30 Ω series terminated outputs, 3-state	74ALVT162241	2 LOW + 2 HIGH	NINV
16 bit buffer/driver, 3-state	74ALVT16244	4 LOW	NINV
16 bit buffer/driver with 30 Ω series terminated outputs, 3-state	74ALVT162244	4 LOW	NINV
16 bit transceiver, 3-state	74ALVT16245	2 LOW	NINV
16 bit transceiver with 30 Ω series terminated outputs, 3-state	74ALVT162245	2 LOW	NINV
16-bit 1-to-4 address driver, 3-State	74ALVT16344	4 LOW	NINV
16-bit 1-to-4 address driver with 30 Ω series terminated outputs, 3-State	74ALVT162344	4 LOW	NINV
18 bit universal bus transceiver, 3-state	74ALVT16500	1 LOW + 1 HIGH	NINV
18 bit universal bus transceiver, 3-state	74ALVT16501	1 LOW + 1 HIGH	NINV
16 bit buffer/driver, 3-state	74ALVT16541	4 LOW	NINV
18-bit universal bus transceiver, 3-State	74ALVT16600	2 LOW	NINV
18-bit universal bus transceiver, 3-State	74ALVT16601	2 LOW	NINV
16-bit address register/driver, 3-state	74ALVT16731	2 LOW	NINV
16-bit address register/driver with 30 Ω series terminated outputs, 3-state	74ALVT162731	2 LOW	NINV
20 bit buffer/driver, 3-state	74ALVT16827	4 LOW	NINV
20 bit buffer/driver with 30 Ω series terminated outputs, 3-state	74ALVT162827	4 LOW	NINV
18 bit latched transceiver with parity generator/checker, 3-state	74ALVT16899	2 LOW	NINV